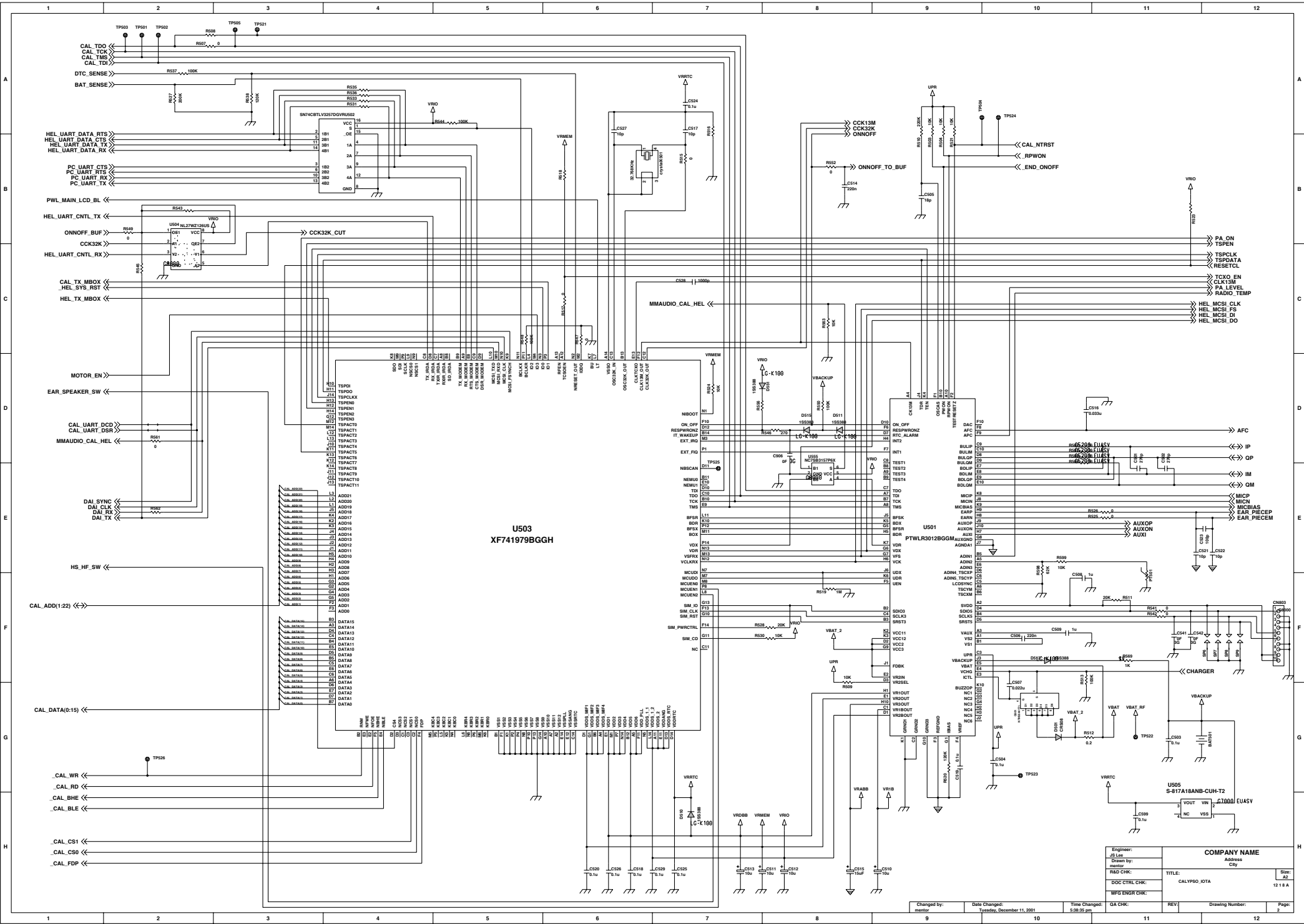


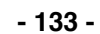
9. CIRCUIT DIAGRAM

Main PCB Circuit (1/5)



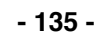
Main PCB Circuit (2/5)





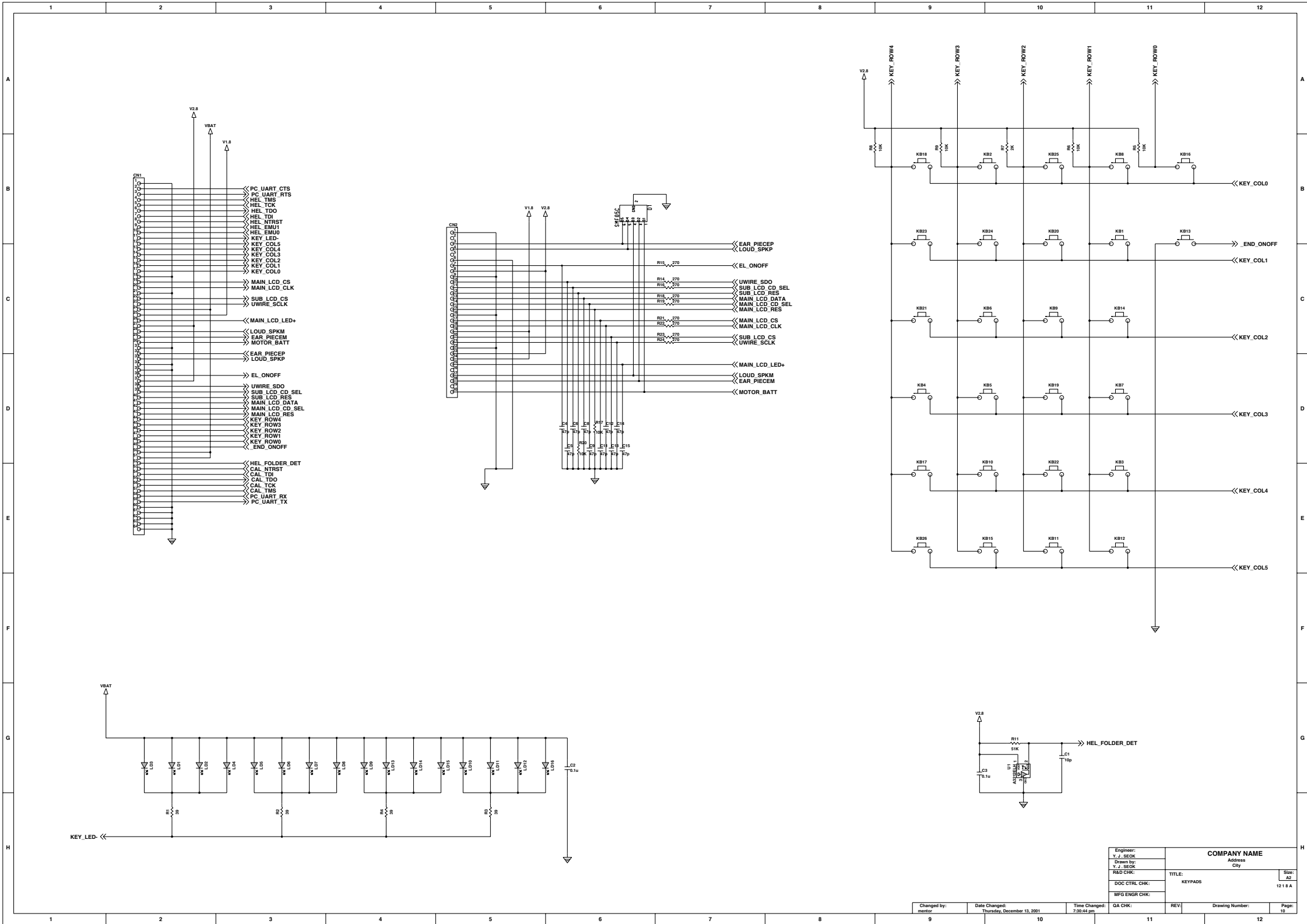
Main PCB Circuit (4/5)



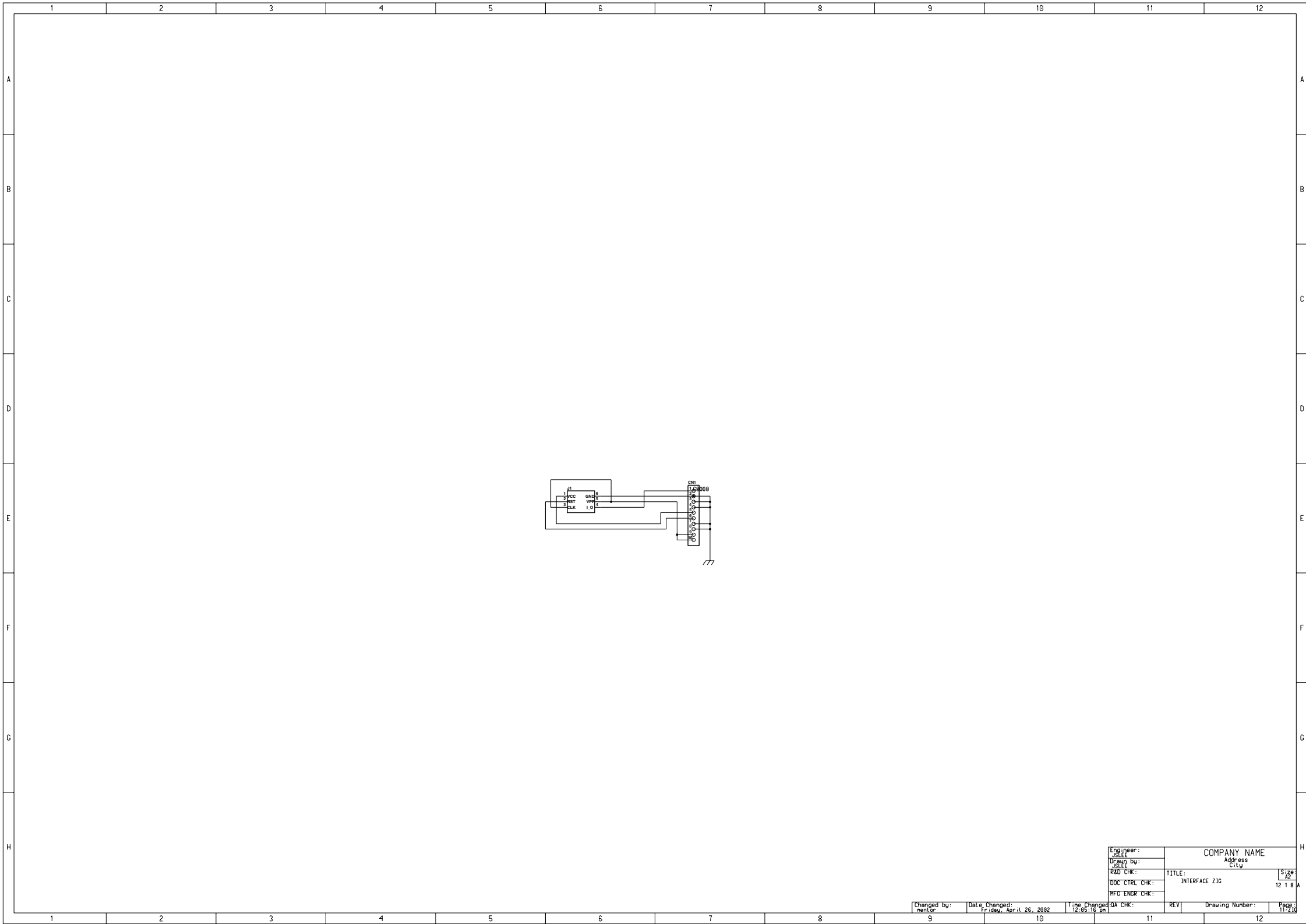


9. CIRCUIT DIAGRAM

Keypad PCB Circuit (1/1)



SIM PCB Circuit (1/1)



Engineer:	JULEE	COMPANY NAME	Size:
Drawn by:	JULEE	Address	A2
Rev:	1	City	12 1 8 A
DOC. CTRL. CHK:		TITLE:	INTERFACE ZIG
PRG. ENGR. CHK:		REV:	

Changed by:	Date Changed:	Time Changed:	QA CHK:
net/or	17-09-2002	12:05:16 pm	

REV:	Drawing Number:	Page:
		11/20